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FOR IMMEDIATE RELEASE

Virtutech® Presents Four Speaking Sessions at 2008 Multicore Expo Japan

SAN JOSE, Calif.—November 4, 2008—Virtutech, Inc., the leader in virtualized software development (VSD) solutions for electronic systems, today announced that company executives will lead four speaking presentations during the 2008 Multicore Expo, November 6-7, 2008 at Meguro Gajoen in Tokyo, Japan. The topics range from a keynote session on VSD as a solution to the disruptive impact of multicore on the semiconductor industry to two technical sessions on putting VSD to work in multicore environments.

Friday, November 7, 2008 from 10:45-11:30 a.m. JST: Virtutech vice president of marketing Michel Genard will present a keynote session on "Virtualization: Infrastructure for Multicore Migration." The presentation will detail how system level virtualization and fast simulation are promising to be as disruptive to the electronics development landscape as server virtualization has been to the data center. Genard will also discuss both current industry trends as well as his own thoughts on how virtualized software development will revolutionize the industry.

Thursday, November 6, 2008 from 3:40-4:10 p.m. JST: Takahashi Takahiro, senior applications engineer at Virtutech, will present "Hybrid Simulation for Multicore Software Developers." This session will demonstrate how full system functional simulation can be combined with cycle accurate processor and accelerator simulation to provide a hybrid simulation environment that meets the needs of multicore software developers.

Friday, November 7, 2008 at 1:10 p.m. JST: Takahiro also will present "Simulating Multicore Computer Systems - How Approximate Can You Be?" During this presentation Takahiro will discuss his experience in what works and when it works when it comes to high-performance reduced-detail simulation of multicore systems. Takahiro's presentation will also highlight how synchronization code behaves when simulated with time quanta, what kinds of software bugs can be found at what levels of detail and why.

Friday, November 7, 2008 from 4:40-5:10 p.m. JST: Takahiro will present **"**Using Virtual Hardware to Debug Multiprocessor Software." This session will address the need for new debug tools and techniques to help software developers make full use of multicore computing systems. Takahiro will also discuss how the use a virtual, simulated, model of a multicore system instead of physical hardware provides control over and insight into the target system, and makes it possible to reliably reproduce problems.

Virtutech will also offer three different demonstrations at their booth during the expo:

- **Multicore demo pod**: Freescale QorIQ P4080 and MPC8641d, Cavium Networks OCTEON

- **Modeling language interoperability: Simics SystemC Bridge™.** This Simics interface provides SystemC-based devices with a scalable, top-down development environment for demanding, high performance product development.

- **Multicore & multi-threads messaging technology:** showing a Simics virtual platform running ThreadX® from Express Logic with Polycore Poly-Messenger™ supporting the first released API MCAPI from The Multicore Association™.

For more information about these presentations, please visit the Multicore Expo Web site at: http://www.multicore-expo.com/common/agenda.php?expo_seq=8

About Virtutech
Virtutech, Inc. is the leader in product development process improvement through virtualized software development (VSD). Virtutech Simics® allows for a revolutionary change in the product development process at a full system level rather than a component level and is the only commercial solution that delivers the four most important criteria for successful deployment of hardware virtualization in the electronics equipment development process: speed, scalability, model availability, and control. Simics customers report reduced time to market, better project risk management, lower capital expenditure, product development cost and maintenance as well as increased quality and individual productivity. Virtutech serves the needs of the world's leading OEMs in the high-performance computing, aerospace and defense, telecommunications, networking and semiconductor industries. Customers include Cisco, Ericsson, Freescale Semiconductor, GE Avionics, Honeywell, IBM, Lockheed Martin, Nortel, Northrop Grumman, MontaVista Software and Wind River. Virtutech is an active participant in organizations to drive adoption of VSD such as ARM Connected Community, Eclipse.org, IBM PartnerWorld, Power.org, OSCI and Spirit Consortium. Virtutech is headquartered in San Jose, Calif. For more information, visit www.virtutech.com.

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